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Substitute for form 1449A/PTO

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Sheet 1 of 2

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|-------------------------------|---------------------|
| <b>Application Number</b>     | 09/940053           |
| <b>Filing Date</b>            | August 27, 2001     |
| <b>First Named Inventor</b>   | Papanikolaou et al. |
| <b>Group Art Unit</b>         | 2631                |
| <b>Examiner Name</b>          | Unknown             |
| <b>Attorney Docket Number</b> | 764164605017        |

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**Examiner  
Signature**

Edward Lang

Date  
Considered

4/12/2006

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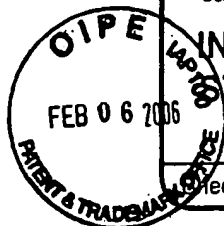
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Sheet 2 of 2

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|------------------------|---------------------|
| Application Number     | 09/940053           |
| Filing Date            | August 27, 2001     |
| First Named Inventor   | Papanikolaou et al. |
| Group Art Unit         | 2631                |
| Examiner Name          | Unknown             |
| Attorney Docket Number | 764164605017        |

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| Examiner<br>Initials* | Cite<br>No.† | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T² |
|-----------------------|--------------|---|----|
| eh                    |              | MAX3801: <a href="http://pdfserv.maxim-ic.com/arpdf/MAX3801.pdf">http://pdfserv.maxim-ic.com/arpdf/MAX3801.pdf</a> , 3.2 Gbps Adaptive Equalizer.   |    |
| eh                    |              | A. J. Baker, "An Adaptive Cable Equalizer for Serial Digital Video Rates to 400 Mb/s," IEEE ISSCC, pp. 174-175 1996.  |    |
| eh                    |              | M. H. Shakiba, "A 2.5 Gb/s Adaptive Cable Equalizer", IEEE ISSCC, pp. 396-397, 1999.  |    |
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Signature

*Eliot Chang*

Date  
Considered

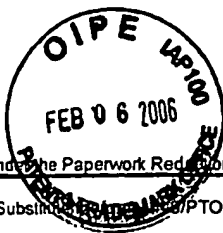
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Sheet 1

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| Application Number | 09/940.053 |
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| Filing Date | 8/27/2001 |
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| First Named Inventor | Papanikolaou et al |
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| Art Unit | 2631 |
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| Examiner Name | Unknown |
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| Attorney Docket Number | 764164605017 |
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## U. S. PATENT DOCUMENTS

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**FOREIGN PATENT DOCUMENTS**

| FOREIGN PATENT DOCUMENTS |                          |   |                                   |  |   |                |
|--------------------------|--------------------------|---|-----------------------------------|--|---|----------------|
| Examiner<br>Initials*    | Cite<br>No. <sup>1</sup> | Foreign Patent Document   | Publication<br>Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines,<br>Where Relevant Passages<br>Or Relevant Figures Appear | f <sup>6</sup> |
|                          |                          | Country Code <sup>3</sup> * Number * Kind Code <sup>5</sup> (# known) |                                   |  |   |                |
| M<br>B                   |                          | EP 1 133 084 A1   | 9/12/2001                         | Tektronix Inc.                                     |   |                |
|                          |                          | WO 98/45955   | 10/15/1998                        | Gennum Corp.                                       |   |                |
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G. L. Lang

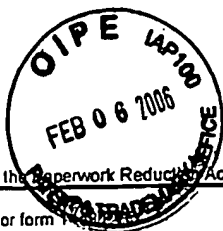
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|   |   | Application Number       | 09/940053           |                        |              |
|   |   | Filing Date              | 8/27/2001           |                        |              |
|   |   | First Named Inventor     | Papanikolaou et al. |                        |              |
|   |   | Art Unit                 | 2631                |                        |              |
|   |   | Examiner Name            | Unknown             |                        |              |
| Sheet   | 2 | of                       | 2                   | Attorney Docket Number | 764164605017 |

| NON PATENT LITERATURE DOCUMENTS |                       |   |                |
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| lh                              |                       | Watanabe et al., "Adaptive Line Enhancers with Discriminated Structures", Electronics and Communications in Japan, Part 3, Vol. 79, No. 11, 1996, pp 57-68<br>XP 000681160, New York.   |                |
| ek                              |                       | Guinea, Stacey, "Analog and Digital Circuit Techniques in Telecom Design for ISDN", International Symposium on Circuits and Systems, pp 137-140, XP000795192<br>New York.   |                |
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| Examiner Signature | <i>Edmund Chung</i> | Date Considered | 4/12/2006 |
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